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Substitute for form 1449A/PTO INFORMATION DISCLOSURE STATEMENT BY APPLICANT (use as many sheets as necessary)		Complete if Known	
		Application / Conf. No. Filing Date First Named Inventor Art Unit Examiner Name Attorney Docket Number	Unknown 10 / 783,216 February 20, 2004 Tim Tuan Unknown 2825 Unknown Garbowski X-1462-1P US
Sheet	2	of	2

OTHER - NON PATENT LITERATURE DOCUMENTS			
Examiner Initials *	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
/L.G./		HE, LEI; "Power Efficient FPGA: Circuit, Fabrics and CAD Algorithms," Presentation on February 13, 2004, 50 pages, at Xilinx, Inc. 2100 Logic Drive, San Jose, CA 95124, available from EE Department, UCLA, at http://eda.ee.ucla.edu/ .	
/L.G./		FPGA 2004 ADVANCE PROGRAM; "ACM/SIGDA Eleventh international Symposium on Field Programmable Gate Arrays, February 22-24, 2004, 6 pages, at Monterey Beach Hotel, Monterey, California, available at http://fpga2004.ece.ubc.ca/	
/L.G./		TAKAHASHI, M. et al.; "A 60-mW MPEG4 Video Codec Using Clustered Voltage Scaling with Variable Supply-Voltage Scheme," November 1998, pages 1772-1780, Vol. 33, No. 11, available from IEEE Journal of Solid-State Circuits, IEEE, 3 Park Avenue, 17th Floor, New York, NY 10016-5997.	

Examiner Signature	/Leigh Garbowski/	Date Considered	09/24/2008
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